HOME PERSONAL COMPUTER **AX-500**

SERVICE MANUAL



CONTENTS

Features 1
Specifications 1
Memory map 3
AX-500 I/O address map 4
Floppy disc system I/O address map 4
Disassembly Procedures 5
Keyboard Unit Disassembly9
Screw Arrangement Diagram 10
Adjustments11
Calendar clock adjustment 12
New LSI Data Table14

008530



FEATURES

- · Separate keyboard with 10 keys
- Equipped with 2 1-Mbyte type 3.5 inch double-sided floopy disc drivers.
- 2 cartridge slot, 1 side slot.
- Capable of outputting each picture of RGB, composite video and RF
- Main recording capacity: 256 Kbytes · Recording capacity for video: 128 Kbytes
- YM-3814 (MSX₂ system) used

Bit map function Clock function

Back-up RAM (16 bytes)

SPECIFICATIONS

CPU to be used: Clock frequency: Wait:

Interrupt:

Z80A or its equivalent (LH0080A) 3.579545 MHz

1 wait cycle inserted into M1 cycle

NMI not used

Interrupt from VDP and external slot is used. Normally, MSX-BASIC interprinter uses 50Hz signal sent out from VDP as an interrupt signal.

(Interrupt mode 1)

Power ON reset or reset switch is

used.

Memory

Reset:

Main memory:

RAM area (with memory mapper

function)

256 Kbytes 64 Kbit × 4 columns

×8 chips

ROM area (including each type of application)

MSX₂ BASIC Ver 2.1 (INT) 32 Kbytes

Exp. BASIC & Disk BASIC 32 Kbytes

ARABIC BASIC 32 Kbytes SAKHR FILES 48 Kbytes ARABIC PAINTER 64 Kbytes ARABIC WP

32 Kbytes

128 Kbytes

Video RAM: Screen Display

VDP to be used: Characters:

Video display processor V9938 Alphanumeric characters, Arabic characters, graphic characters

256 types

8×8 (6×8 screen 0) dot Refer to Display mode for resolution,

pattern size, color and number of sprites.

Calendar Function and Battery Backup Function

Calendar:

Built in YM3814 (MSX2 system) Compatible with RP5CO1 (made by

RICOH) on software

Error; within 60 seconds/month Calendar (year, month, day) Time (hour, minute, second)

Back-up:

Battery:

Calendar clock & RAM (16 bytes) built in YM3814 is backed up when

main power is turned OFF. 2 AAA type batteries are used

(about 1-year service life)

I/O Interface

Separate keyboard:

13 pin DIN receptacle connector

Step sculpture type (Only 10 key is step type.)

Alphanumerical characters, special characters and Control and special keys 16 Cursor keys 4 Function key (10 functions available by shifting).... 5 10 key 16 CODE lock and CAPS lock indicated by LED

Audio cassette interface:

8 pin DIN receptacle connector

Baud rate: 1200/2400bps FSK system (changed by software)

With remote control function

Printer interface:

Unphenol 14 pin female connector

Conforming to 8 bit parallel centronics specifications

TTL level

Generalized input/output (JOYSTICK etc.) 2 port:

D sub-type 9 pin male connector \times 2 TTI level

A/V output

Sound output 8 octave triple chord output, special

sound, noise RCA pin jack 8 octave triple chord section $47k\Omega$ load, $0.40\pm0.10Vrms$ MAX (440Hz square wave, 1ch output)

Special sound section

47kΩ load, 0.20 ± 0.05Vrms MAX

(440Hz square ware output)

PAL composite video output

75Ω RCA pin jack

RF output UHF 36 channels

RCA pin jack

RGB output Signal corresponding to EIAJ21 pin output

8 pin DIN receptacle connector ROM cartridge slot (SLOT A, SLOT B);

50P Female connector of MSX specifications

Side cartridge slot (SLOT #33):

60P Card edge connector (specially for YAMAHA)

Power section			
Power specifica-	Primary side	100V ~ 3	240V
tions:		$(\pm 10\%)$	50/60Hz
	Secondary side	5V	3.0A
		+12V	0.5A
		-12V	0.2A
Power external	5V ± 5%	1A (max	d)
output	siot	300mA	× 2
	I/O port	50mA ×	2
	Side slot	300mA	
	$+12V \pm 10\%$	100mA	(max)
	$-12V \pm 10\%$	100mA	(max)

General specifications

Power:

Power consumption: Max, Operating conditions: Temperature

Temperature 5~35°C Humidity 20~80%

21W

Dimensions (Width × height × depth): AX-500 395mm ×

395mm × 80mm × 380mm 417mm × 36mm × 175 mm rd 1500mm ± 50mm

Keyboard Curled cord Weight: Main Unit Keyboard

6.7kg 1.8kg 2000mm±50mm

3.5 inch FDD

Number of built-in drivers:

AX-500 Model

Specifications of drivers to be used: Product number JU-363-08 type

Disc to be used 3.5 inch double-sided, double-

density, double-tracks (2DD type)
ecording capacity 1 Mbyte unformatted (both sides)

Recording capacity 1 Mbyte unformatted (both sides) 720 Kbytes formatted (both sides) Recording density 8717 BPI

Recording density 8717 BPI
Track density 135 TPI
Number of cylinders 80 cylinder

Number of tracks 160 tracks when both sides are

used. 100ms at average

Step rate; 6ms

Access time

Specifications of controller:

FDC MB8877M (Fujitsu)
Data separator SED9420-CAC (SEIKO)

Analog filter changing type VFO

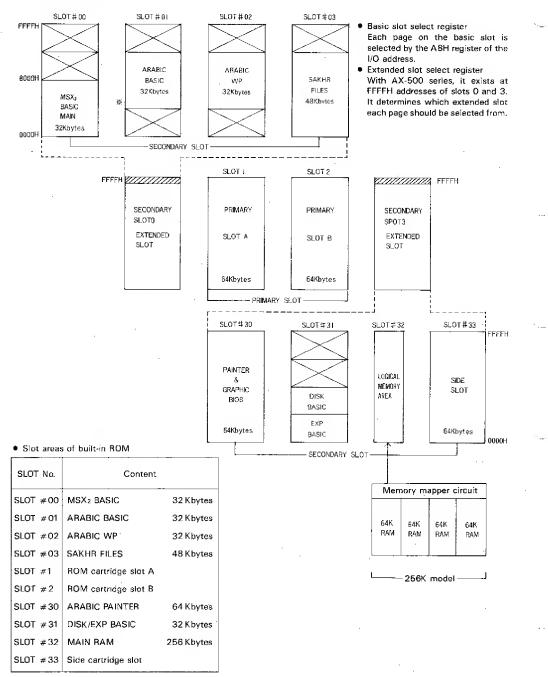
2 units

Display Mode

AC cable:

	Resolution	Pattern Size	Color	Sprite Number	V-RAM Area
Text I (SCREEN 0)	256 x 192 (MAX 40 characters)	6×8 (256 types)	2 out of 512	No	16K
Text II (5CREEN 0)	512 x 192 (MAX 80 characters)	6×8 (256 types)	2 out of 512	No	16K
Multicolor (SCREEN 3)	64×48	4×4	16 out of 512	4/line out of 32	16K
G I (5CREEN 1) 256 × 192		8 × 8 (256 types)	16 out of 512	4/line out of 32	16K
G II (5CREEN 2) 256 × 192		8 × 8 (768 types)	16 out of 512	4/line out of 32	16K
G III (SCREEN 4) 256 × 192		8 × 8 (768 types)	16 out of 512	8/line out of 32	16K
G IV (5CREEN 5) 256 × 192		8rt Map	16 out of 512	8/line out of 32	32K
G V (SCREEN 6)	512×192	Bit Map	4 out of 512	8/line out of 32	32K
G VI (SCREEN 7)	512 × 192	Bit Map	16 out of 512	8/line out of 32	128K (2 screens)
G VII (SCREEN 8)	256 × 192	Bit Map	256 colors simultaneously	8 line out of 32	128K (2 screens)

MEMORY MAP



■ AX-500 I/O ADDRESS MAP

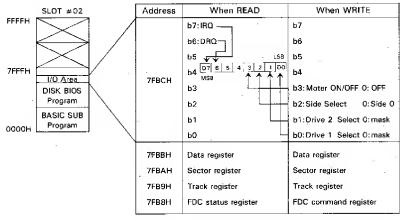
The following table shows the AX-500 $I\!/\!O$ map.

	21101111119	bits different trice (ive trice)	
FFH FEH FDH -FCH	P/W R/W R/W R/W	Map register on p.3 Map register on p.2 Map register on p.1 Map register on p.0	Each block of M0 to M3 is assigned when the power is ON and when resetting.
В5H В4H	R/W W	Calendar clock (Built in MSX2 system)	Data (READ/WRITE) Address latch (WRITE)
ABH AAH A9H A8H	R/W R/W R R/W	Parallel port (equivalent to µPD8255)	Mode set (bit set, reset) Keyboard strobe, cassette control PP1 sound Keyboard return read Primary slot select register
A2H A1H A0H	R W W	Sound generator, joy port, etc.	Data (READ) Data (WRITE) Internal address latch (WRITE) SSG internal register address set
9BH 9AH 99H 98H	W W R/W R/W	VDP-9938	Internal register indirest assignment (WRITE) Color palette register access (WRITE) Command access to VDP/status read Data to V-RAM READ/WRITE
91H 90H	W R/W	Printer port	Data output (printer) Busy (READ) bit 1 Strobe (WRITE) bit 0
47H 46H	R/W W	·	Bit pattern (WRITE)/color code (READ) FG, BG color code (WRITE)
42H 41H	R/W W		Back-up RAM (data) READ/WRITE Back-up RAM address latch
40H	R/W		Device ID number register

■ FLOPPY DISK SYSTEM I/O ADDRESS MAP

The I/O address for the floppy disk system is assigned to a part of the memory area of application software by using the memory mapped I/O system.

Floppy disk system I/O address map

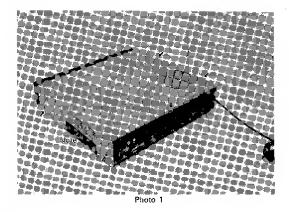


7FB8H to 7FBBH are internal registers of FDC (MB8B77).

■ DISASSEMBLY PROCEDURES

Top Cover Removal

- · Unscrew 4 screws on both sides.
- Unscrew the screw at the upper center of the rear panel.
- Remove the top cover by lifting it up while using care for the groove of the side slot guide.



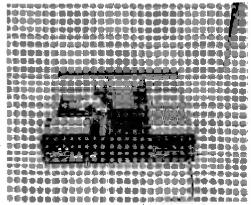


Photo 2

AC Inlet Ass'y Removal

- Unscrew 2 screws fixing the AC inlet ass'y. (Photo. 3-A)
- Disconnect 2 power lines from the AC inlet ass'y.
 Connector for front panel ass'y (Photo. 3-C)
 Connector for CPU main circuit board (CN304) (Photo. 3-D)
- Remove the AC inlet ass'y by lifting it up.
 Note) The power unit (power circuit board) is installed in the AC inlet ass'y.

Rear Panel Removas

- Unscrew 5 screws fixing the rear panel.
- Remove the rear panel by opening it from the lower side while sliding it upward a little.

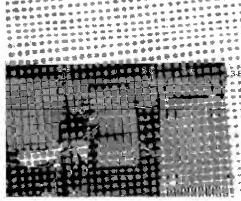


Photo 3

Front Panel Ass'y Removal

- Unscrew 2 screws in the apper part of the front panel ass'y (Photo, 3-B)
- Disconnect the flat cable (CN308) to the CPU main circuit board. (Photo, 3-E)
- Remove 4 plastic stoppers of the front panel ass'y from the bottom of the main unit by pushing them with a screwdriver. (Photo. 4)

 * With the YIS805/128 model, if a floppy disk driver

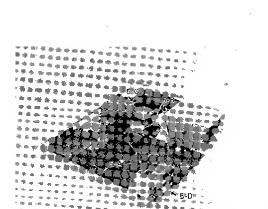


Photo 5

Video Encloder Circuit Board Removal

- Pull off the harness (CN201) from the CPU main circuit board. (Photo. 6-A)
- Unscrew 4 screws.
- Remove the video encoder circuit board by pulling it rearward.

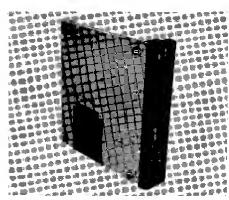
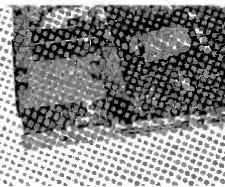


Photo 4

CPU Main Circuit Board Removal

- Pull off the battery cable ass'y (CN309), (Photo, 5-A)
- · Pull off the power line to the floppy disk driver and 34P flat cable from the connector on the floppy disk driver. (Photo. 5-B)
- Disconnect the connector of the video circuit board. (Photo. 5-C)
- . Unscrew 6 screws fixing the CPU main circuit board and 2 screws of the side slot ground. (Refer to the screw list.)
- Remove 2 PC supports (Black) in the center of the CPU main circuit board.
- Remove the keyboard connector from the bottom chassis by loosening 2 screws. (Photo. 5-D)



FDD Floppy Disk Driver Removal

- Unscrew 2 screws fixing the FDD shielding ass'y.
 Pull out the connector to the FDD (with 34 pin flat cable and power cable).
- Unscrew 4 screws fixing the video encoder circuit board. (Photo. 6)
- · Remove the FDD shielding ass'y with 2 floppy disk drivers mounted as they are.
- Note: As the FDD shielding ass'y is held by catches of the main body chassis, slide it forward for

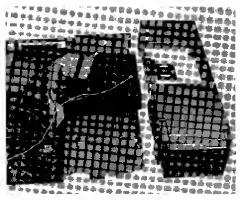


Photo 7

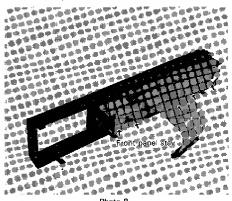


Photo 8

- · Pull off the connector of the micro switch ass'y from
- the slot circuit board. (Photo. 9-A)
 Unscrew 2 screws (3×8) fixing the cartridge case and remove the cartridge case ass'y from the front panel. (Photo. 9-B)

Front Panel Ass'y Disassembly

- Unscrew 4 screws (3 × 16) fixing the front panel stay.
 Remove the stay by sliding it rearward gradually while using care for the 60 pin flat cable.

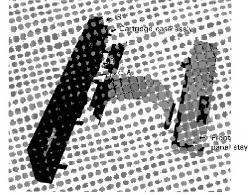


Photo 9

Note that a fine ground wire is connected on the back of the 60 pin flat cable. Be careful not to move the cable with force in removal, for it may break the fine ground wire.

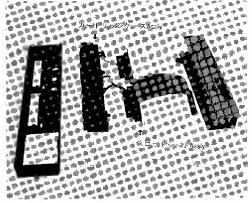


Photo 10

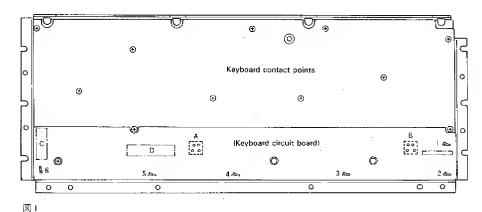
■ KEYBOARD UNIT DISASSEMBLY

Keyboard Circuit Board Removal

- (1) Unsolder A and B in Fig. 1.
- (2) Straighten the catches numbered from 1 to 6 in the figure.
- (3) Remove the flexible circuit board from the connectors C and D as shown in Fig. 2.
- (4) Remove the keyboard circuit board gradually.

Keyboard Contact Point Removal

(1) Unscrew 13 special screws shown in Fig. t, and the contact point of the keyboard can be removed.



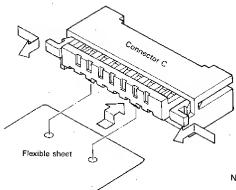
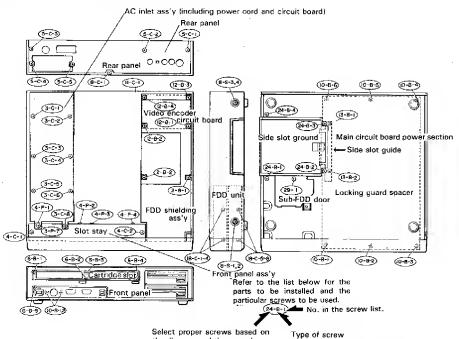


Fig. 1

Fig. 2

Note: Insert the flexible sheet while fitting the catches of the connector into the holes in the flexible sheet.

SCREW ARRANGEMENT DIAGRAM



Select proper screws based on the diagram and these marks.

- Type of screw
 8:8 tight (binding tapping screw)
 C:C tight (binding machine screw)
 P:P tight (binding tapping screw)
- S:S tight (binding tapping screw)

Screw List

	Part to be installed	Type of screw and quantity					
No.	Part name	+ binding B tight 3×8	+ binding C tight 3×6	+ binding P tight 3 × 14	Cup S tight	Coin slotted pan head	Total (pcs.
24	Side slot guide (to bottom)	4					4
10	Main circuit board (to bottom)	6			2 ^{(to K8} connector)		8
13	Side slot ground	2					2
2	FDD shielding ass'y (to bottom)	2	·				2
12	Video circuit board (to FDD shielding ass'y)	4					4
18	FDO unit (for 2 units)		8				8
6	Front panel (cartridge slot LED)	5					5
4	Slot stay and circuit board (to front panel)		2 (to bottom)	4			6
3	Power supply unit (to shielding)		8				8
5	Rear panel		5				5
8	Top cover		1 (to rear panel)		4		5
29	Sub-FDD door	1				. (1)	1
	Total (pcs.)	24	24	4	6	(1)	58

ADJUSTMENTS

< Voltage Adjustment >

ltem	For			
Conditions	Connect power circuit to CPU board. No load applied to each slot (such as game cartridge) of CPU board. No paripheral aquipments (such as printer and joy stick) should not be connected.			
Voltaga adjustment	+5.10V±5% Connector CN304 (2 pin) and GND			
Voltage Confirmation	-12V Connector CN304 (4 pin) and GND: -12V±10.0% +12V Connector CN304 (1 pin) and GND: +12V±10.0%			

With each unit connected, adjust VR101 in the power supply unit so that the output voltage is obtained at the voltage input terminal of the CPU circuit board CN304 as described in the above table.

< Adjustment and Confirmation of Calendar Clock and Back-up Circuit >

Calendar clock adjustment

Write "00118" into the test register of the calendar clock, set to TEST.3 mode and adjust TC301 on CPU circuit board so that the frequency of the output signal at ALARM terminal of YM3814 (S-1985) 87 pin satisfies the following specification. (Refer to p.12 for the calendar clock adjustment.)

Item	For
Calendar clock	YM3814 (S-1985) 87 pin 8.192 [KHz] ± 0.2 [Hz]

Confirmation of battery back-up operation

Using BASIC command or MSX-DOS command, put time/ year, month and day in memory.

Turn OFF the power once and ON again, then check to make sure that the time/year, month and day is maintained correctly.

Confirmation of battery back-up circuit terminal voltage

Check to make sure that the voltage at 86 pin BVSS $\overline{\text{terminal}}$ of YM3814 (S-1985) satisfies specifications given below.

Power	Value	
When ON	0~0.7V	

Power	Value
When OFF	- 2.2V or less

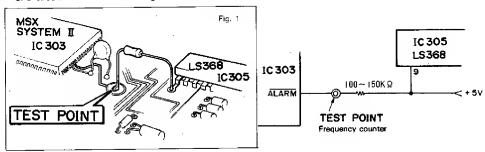
< Adjustment and confirmation of picture circuit >

Check or adjust for the following at the connecting point A/V connector (CN312) on CPU circuit board.

Pin No.	Name	ltem
1	AUDIO	Confirm sound output.
2	VIDEO	Connect video unit and confirm composite video signal.
3	GND	
4	sc	Adjust with TC302 for; CPU CLOCK TTL level f = 3.57945 [MHz] ± 500 [Hz].
5 .	NC	NON CONNECT
6	SYNC	Negative polerity, period signal output TTL level
7	GND	
8	В	
9	G	Confirm that voltage is between 0.85V and 1.10V when screen dieplay is white
10	R	1)'

■ CALENDAR CLOCK ADJUSTMENT

1. Preparation for adjustment
Connect a 100K to 150K pull-up resistor between the TEST point and +5V (IC305, 9 pin: LS368) of the CPU circuit board as shown in Fig. 1.



With the CPU circuit board in operating state, prepare BASIC programs as follows.

10 OUT & HB4, & HE

20 OUT & HB5, & H3

30 END

2. Measurement and adjustment

With a frequency counter connected to the TEST point, run the program and adjust TC301 (variable trimmer) so that f=8.192KHz ± 0.2Hz is satisfied.

• SED9420COB (DATA SEPARATER)

Pin No.	Pin Name	Function
1	OSC1	Inverting amplifier gate input in crystal oscillation circuit
2	OSC2	Inverting amplifier drain output in crystal oscillation circuit
3	CLK1	Clock input for FDC Standard floppy (8 inch): 8MHz Mini floppy (5 inch): 4MHz
4	TEST2	TEST terminal (input terminal with pull-up resistor)
7	RD DATA	Input terminal (with pull-up resistor) of read data signal from floppy disk driver unit (FDD)
8	WINDOW	Output terminal of data window signal for separating DATA signal into clock pulse and data pulse.
9	DATA	Output terminal for data signal produced from RD data signal. It is separated into data pulse and clock pulse by window signal which is read to FDC.
10	MFM/FM	Terminal for changing recording system between single density and double density (with pull-up resistor) Double density (MFM recording): High level Single density (FM recording): Low level
11	MIN/STD	Terminal for changing floppy disk type between mini type (5 inch) and standard type (8 inch) - 5 inch floppy disk: High level - 8 inch floppy disk: Low level
12	Vss1	Ground terminal (digital system)
13	Vss2	Ground terminal (analog system)
14	CONTROL	For control of VCO (Voltage Controlled Oscilator) Input terminal for voltage (output voltage of loop filter)
15	OFFSET	Input terminal of offset voltage for correcting oscillation center frequency of VCO. Offset voltage can be also generated automatically by externally attaching capacity.
17	LPF	Connecting terminal of loop filter (output terminal of charge pump)
18	TEST1	Test terminal (not connected usually)
19	WCLK	
20	CR	C-R externally attached terminal for timer circuit
21	CLK2	MB8877 system, clock output terminal for FDC · B inch floppy : 2MHz · 5 inch floppy : 1MHz
22	TRIG IN	Trigger input terminal for timer circuit (with pull-up resistor)
23	TM OUT	Timer circuit output terminal
24	Voo	+5V power voltage terminal

Note) The input terminal with pull-up resistor is pulled up by a resistor whose standard value is 100kΩ. As noise tends to affect easily when used open, it is recommended to connect directly to Voo if the input terminal is used High level.

NEW LSI DATA TABLE

• MB-8877 (FDC)

Pin No.	Pin Name	1/0	Function
1			
2	WÉ	1	Write request signal to internal register
3	C \$	1	FDC chip select signal
4	ŘE	1	Read request signal to internal register
5	AO	1	Address line to select internal register
6	A1	1	Address line to select internal register
7	DALO	1/0	Data line
8	DAL1	1/0	
9	DAL2	1/0	
10	DAL3	1/0	
11	DAL4	1/0	
12	DALE	I/O	
13	DAL6	i/O	
14	DAL7	1/0	
15	STEP	0	Head movement pulse to FDD
16	DIRC	0	Head movement direction select signal to FDD
17	EARLY	0	Shift write data to the faster side Compensation terminal
18	LATE	0	Shift write data to the later side
19	MR		FDC chip reset signal
20	Vss	_	Ground
21	Vpp	_	+5V power supply
22	TEST		Pulled up
23	HLT		Pulled up
24	CLK	1	Clock pulse input
25	RG	1	Read gate
26	RCLK	1	Read data window pulse
27	RAW READ	1	The raw data directly obtained from the disk
28	HLD	. 0	Press the head on the diskette
29	TG43	0	Head occurata larger track than 44
30	WG	0	Data is being written on the disk
31	WD	0	Write data for the diskette
32	READY	_	Pulled up
33	WF/VF	-	Write data error generated in diskette
34	TROO	1	Head occure on track 00
35	ĪP	1	Index hole detection
36	WPRT	1	Diskette write disable
37	DDEN	- 1	Access a double density diskette
38	DRQ	0	Data request IRQ signal
39	IRQ	0	IRQ signal
40	Vas	_	+ 12V power supply

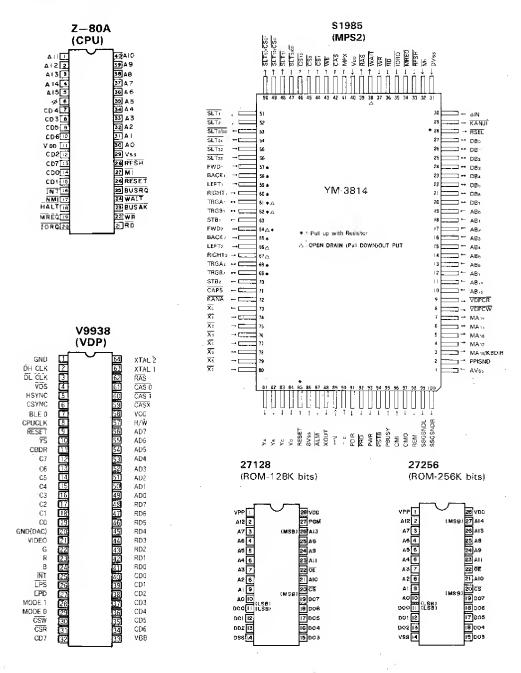
YM-3814 (MSX2-SYSTEM)

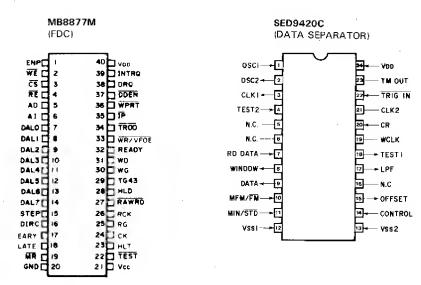
Pin No.	Pin Name	1/0	Function
1	AVss	_	Analogue ground
2	PPISNID	o	Software controlled sound signal
3	KBDIP	0	Keyboard bus buffer direction signal
4	MA17	0	Address line-for memory mapper circuit (256KB)
5	MA16	ō	Address line for memory mapper circuit (128KB)
6	MA15	0	Address line for memory mapper circuit (64KB)
7	MA14	0	Address line for memory mapper circuit (32KB)
8	VDPCW	o	VDP write enable signal & chip select
9	VDPCR	Ŏ	VDP read enable signal & chip select
10	AB15	Ī	Address line from Z80A CPU for page select etc.
11	AB14	1	Address line from Z80A CPU for page select etc.
12	AB7	li	Address line from Z80A CPU for I/O erea select etc.
13	AB6	i	Address line from 200A GFG for I/O drea select etc.
14	AB5	i	
15	AB4	i	
16	AB3		
17	AB2	l i	· ·
18	AB1	!	
19	ABO	l Lun	David David (co. 7004, CDU)
20	DB7	1/0	Data Bus for Z80A CPU
21	D86	1/0	· ·
22	DB5	1/0	
23	DB4	1/0	
24	DB3	1/0	
25	DB2	1/0	·
26	DB1	1/0	
27	DBO	1/0	
28	RSEL	1	Secondary slot register select signal input
29	KANJI	0	KANJI ROM Select signal
30	φIN	1	CPU clock signal 3.579545MHz
31	DVss	-	Digital ground
3Z	M1	1	Instruction fetch signal from Z80A CPU
33	RFSH		Refresh signel from Z80A CPU
34	MREQ		Memory request signel from Z80A CPU
35	ΙΟRΩ	1	I/O port request signal from Z80A CPU
36	RD		Read request signal from Z80A CPU
37	WR		Write request signal from Z80A CPU
38	WAIT	0	Wait timing signal to Z80A CPU
39	RAS	0	RAS signal to D-RAM
40	VDD	-	+5V power supply
41	MPX	0	Multiplex signal for D-RAM address bus
42	CAS	0	CAS signal to D-RAM
43	WE	0	WRITE enable signal for D-RAM
44	CS1	0	ROM chip select signal (4000H ~ 7FFFH)
45	CS2	0	ROM chip select signal (8000H~BFFFH)
46	CS12	0	ROM chip select signal (4000H~BFFFH)
47	SLT00	0	Secondary SLOT 00 select signal
48	SLT01	0	Secondary SLOT 01 select signal
49	SLT02	0	Secondary SLOT 02 select signal
50	SLT03	0	Secondary SLOT 03 select signal

.

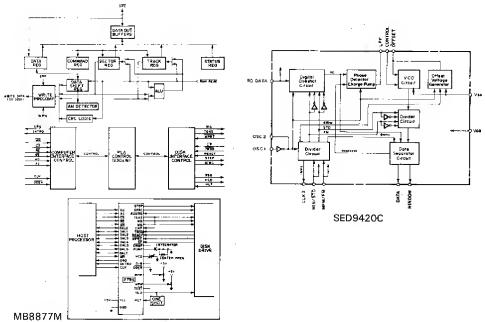
Pin No.	Pin Name	1/0	Function
51	SLT1	0	Primary SLOT1 select signal
52	SLT2	0	Primary SLOT2 select signal
53	SLT30	0	Secondary SLOT30 select signal
54	SLT31	0	Secondary SEOT31 select signal
55	SLT32	0	Secondary SLOT32 select signal
56	SLT33	0	Secondary SLOT33 select signal
57	FWD1	1	Joystick poart 1 signal
58	8ACK1		
59	LEFT1		
60	RIGHT1		
61	TRGA1	1/0	
62	TRGB1	1/0	
63	STB1	0	
64	FWD2	1	Joystick poart 2 signal
65	BACK2	1	
66	LEFT2	1 ;	
67	RIGHT2		
68	TRGA2	1/0	
69	TRGB2	1/0	
70	STB2	0	
71	CAPS	0	CAPS LED ON/OFF signal
72	KANA	0	KANA LED ON/OFF signal
73	<u>xo</u>		Keyboard return signal
74	X1	;	Reyboard Tetarii Signal
75	X2	1 :	
76	X 3	1 i	
77	X 4	1 i	
78	X 5	1 i	
79	X 6	1 :	
80	X 7	1 i	
81	YA	.	Keyboard scanning signal
82	YB	0	Reyboard Scarning Signal
83	YC	0	
84	YD	0	
85	RESET		Reset control signal ("H" enable)
86	BVss		Buck up battery for timer (& for memory)
87	ALM	0	Alarm signal from timer
88	Xout	0	Timer clock signal to quarty circuit
89	Xin		Timer clock signal from quarty circuit
90	Voo	<u>.</u> .	+5V power supply
91	PDIR	0	Printer data bus direction control signal
92	PRD	0	Printer port read request signal
93	PWR	0	Printer port request signal
94	PSTB	0.	Printer STROBE signal
95	PBUSY	0.	Printer BUSY signal
96	CMI		CMT read signal (data signal)
97	CMO	0	CMT write signal (write data)
98	REM	0	CMT motor ON/OFF control signal
99	SSGSNDL	0	SSG Left sound signal
100	SSGSNDR	0	SSG Right sound signal
100	JOUNDIN	"	Soci milita social signal

■ LSI Pin Configuration



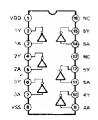


BLOCK DIAGRAM



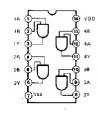
• 7404 Hex Inverter

● 7406

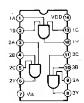


• 7408

Quad 2 Input AND

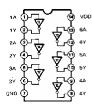


• 7411 Triple 3 Input AND



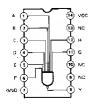
• 7414

Hex Inverter

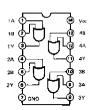


• 7430

8 Input NAND



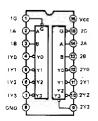
• 7432 Quad 2 Input OR



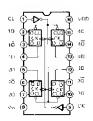
• 74138 3 to 8 Demultiplexer

• 74139

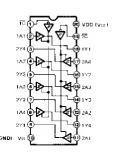
Dual 2 to 4 Demultiplexer



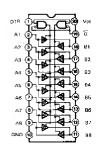
• 74175 Quad D-Type Flip-Flop



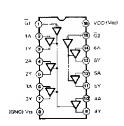
• 74244 Octal 3-State Bus Buffer



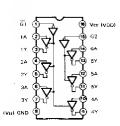
• 74245 Octal 3-State Bus Transceiver



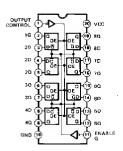
• 74367 Hex 3-State Bus Buffer



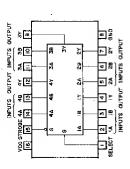
• 74368 Hex 3-State Bus Inverter



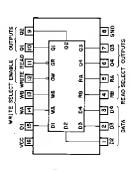
• 74373 Octal 3-State D-Type Latch



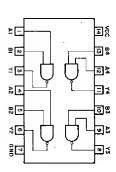
●HC157 Quand 2 to 1 Multiplexer



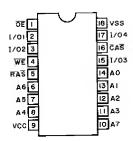
• 74LS670



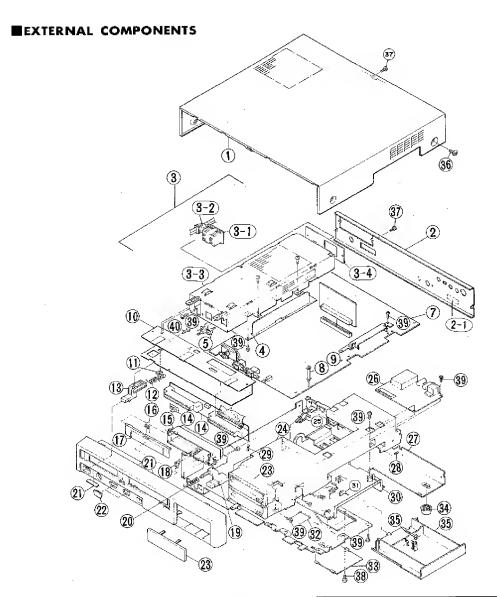
• 74LS00



• MB81464 (D-RAM 64K x 4 bits)



HOME PERSONAL COMPUTER AX-500 PARTS LIST



	Ref. No.	Part No.	Description	品 名	Remarks	ランク
ж	1	VB 75 73 00	Top Cover	トップカバー	-	
**	2	VB 65 59 00	Rear Panel	リアパネル	ı İ	
**	2-1	VB 34 77 00	Label	SER.NO_ ラ ベ J		
35	3	VB 65 07 00	AC Inlet Assembly	ACインレットAss'y		
	3-1	VB 93 46 00	AC Outlet .	電源コネク	7	*

≆ : New Parts (NR)

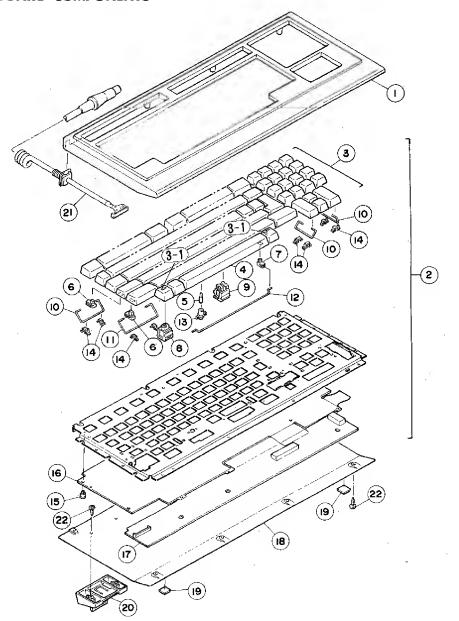
ランク- Japan only

	3-2 3-3 3-4 4 5	MX 55 03 10 VB 65 13 00 VB 65 14 00			電源ケーブル		
* *	3-4 4 5	VB 65 14 00	PW Upper Shield		電源ケーノル		
*	4 5 6				PWアッパーシールド		
····	5 6	25 25 20	PW Panel		PWパ ネ ル		
	6	VB 65 75 00	Power Supply Unit		電源ユニット		
		VB 47 50 00	Power Switch		パワースイッチ		1
*		VB 84 79 00	Circuit Board	ROM Board	R O M シート		1
	7	VB 64 59 00	Circuit Board	CPU	СРИシート		<u> </u>
	8	VB 34 68 00	PC Saport		P C サポート		
- 1	9	AA 55 40 20	Side Slot Earth		サイドスロットアース		
*	10	VB 33 05 00	Slot Stay		フロントスロットステー	74.44	1
	11	VB 75 79 00	Rod Holder		ロッドホルダー		
*	12	VB 75 76 00	Switch Spring		スイッチスプリング		
	13	VB 32 97 00	Switch Button		スイッチボタン		
*	14	VB 22 59 00	Circuit Board	SLOT	S た O T シ ー ト		"
栎	15	VB 32 93 00	Cartridge Slot		カートリッジスロット		
	16	VB 33 07 00	Slot Door Spring		スロットドアスプリング		
	17	VB 32 94 00	Upper Slot Door		アッパースロットドア		
	18	VB 34 76 00	Micro Switch Assembly		マイクロSW Ass'y		
	18	VA 04 10 00	Micro Switch	SCL101U	マイクロスイッチ		
	18	LB 10 11 30	Terminal for Micro Switch		圧 着 端 子		
	18	LB 00 90 30		3P	コネクタハウジング		
	19	VB 32 96 00	Cartridge Slot Eject		カートリッジスロットエジェクト		
	20	VB 34 63 00	Eject Spring		エジェクトスプリング		
L	21	VB 65 65 00	Front Panel		フロントパネル		
	22	CB 55 40 10	JOYSTICK Cover		ジョイスチックカバー		
	23	VB 07 24 00	FDD Unit		FDDユニット	-	
_	24	VB 33 04 00	FDD Shield		FDDシールド		
-	25	VB 75 78 00	Cable Clip		ケーブルクリップ		
_	26	VB 65 77 00	Circuit Board	Video	ピデオシート		
	27	VB 33 01 00	Bottom Chassis		ボトムシャーシ	7177878.2	
_	28	AA 62 73 10	GND Terminal		G N D ターミナル		
_	29	CB 83 19 80	Spacer	KGLS-10R	ロッキングカードスペーサ		
	30	VB 32 98 00	Side Slot Guide		サイドスロットガイド		4
	31	LB 10 11 30	Terminal		圧 着 端 子	.	
	31	LB 00 90 30	Connector Housing	3P	コネクタハウジング		_
	31	8B 55 00 60	Terminal (+)	MSX2	バッテリー端子(+)		+
_	31	BB 55 00 70 NB 55 27 90	# (-)	MSX2	# (-)		+
-	32	VB 33 00 00	Battery Cable Assembly Battery Lock		バッテリーケーブルAss'y	III II TANALIS.	
	33	VB 33 00 00 VB 33 06 00	FDD Door		バッテリーロック		1
	34	VB 34 59 00	Leg		F D D ド ア トランレッグ		1
1	35	VB 65 70 00	Side Slot Cover Assembly		サイドスロットカバーAss y		+
	35	EX 55 00 10	Bind Head Screw	3.0×30			+
-	36	EK 36 50 20	BW Bind Head Screw	4.0X6 FCM3BL	コインスリワリツキネジ		+
_	37	ED 33 00 66	Bind Head Screw	3 0X6 FCM3BL	B W ヘッドオジ バインドCタイト		+
	38	VB 34 65 00	Pan Head Boning Screw	3 0×6 FCRM3BL	コインナベ+Cタイト		+
	39	Ei 33 00 86	Bind Head Tapping Screw	3.0X8 FCM3BL	パインドタッピングネジ		+
_	40	Ei 33 01 46	ri	3.0X14 ZMC2BL	" " " " " " " " " " " " " " " " " " " "	<u> </u>	+ -
\vdash			,	5.5.(T) EMOLDE	"		1 1
					-x · -		+ +
							+ -

: New Parts (NR)

ランク: Japan only

■KEYBOARD COMPONENTS



	1					
Ref No	Part No		Description	品 名	Remarks	ランク
1	VB 65 79 00	Upper Case		アッパーケース		
2	VB 58 28 00	Keyboard Unit		キーボードユニット		
3		Key Top Set(other S,C,K)	 	キートップセット	A7KN037	
3-1	CX 65 73 10	n (for S,C,K)	for LED KEY	キートップセットLED用	D2KN0011	
4		Key Top for SPACE Bar	1	スペースバーキートップ	i	
5	1	Key Top Guide Pin	SPACE Bar	キートップガイドビン		+
				レバーフック(キートップ用)		-
- 6	AX 55 02 60		. Key Top(Middle) SPACE Bar		21KC022	
7	AX 55 02 70			〃 (スペースキー用)		
8		Switch Assembly	CAPS, CODE	スイッチ駆動 Ass'y	***	ļ
9	KX 55 04 60	Push Switch	for CAPS, CODE	ブッシュスイッチ		
10	AX 55 02 20	Lever	l= 1.75	ν <u>κ</u> –	21KC005	
11	AX 55 02 30	и	l=2.5	н	21KC006	
12	AX 55 02 40	n .	for SPACE Bar	н	2 1KC020	
13	AX 55 70 00	Guide	for SPACE Bar	スペースパーガイド	16KC004	
14	AX 55 70 10	Lever Hinge		レバーヒンジ	19KC003	
15	EX 55 00 20			特殊オジ	50KN001	1
16	i	A Point of Contact	-		B2KN020	
17		Circuit Board Assembly		基 盤 Ass'y		1
ŀ <i>''</i>			SN74LS31N	1 C		
			SN74LS125AN	"		
	iG 04 96 50	И				
	iG 12 41 00		SN74LS145N	"		
	iG 05 01 00		SN74LS175N	"		
	iC 07 52 20		2SC752	トランジスタ		
	iF 00 03 30	Diode	iS188	9 1 t - F		
	FG 74 34 70	Ceramic Cap.	4700PF	セラコン	•	
_	FJ 14 B2 20	Electrolytic Cap.	220µF 16V	ケミコン		
·	LX 55 07 30	Connector		コネクタ	4N16005	
	LX 55 07 40	н —		n,	4N16006	
	LX 55 07 50			n,	4N00605	Ī
18	VB 33 16 00			ボトムケース		1
19	VB 34 80 00			スペリ産		1
20		Snap Foot Assembly	-	スナップフットAss'y		1
_	VB 34 79 00		13P	東線キット	1	1
21		·			PACK	
22	El 33 00 86	Bind Head Tapping Screw	3XB FCM3BL	バインドタッピングネジ	PACK	4
		,				
						-
		·		•		<u> </u>
					44477	
	i					
<u></u>						
			· ·	· · ·		
	 	<u> </u>				
	 	-			-	1
- -						
	-			- ·		
				-		
	1 .0=		<u> </u>			<u> </u>
	•				1	1
	T			170		[

※ ∶ New Parts (NR)

■ELECTRONIC PARTS

	Ref. No.	Part No		Description	品 名	Remarks	ランク
*		VB 64 59 00	Circuit Board	CPU	CPU>-+	AX-500	
N.		72 04 00 00					
	IC337	IG 12 19 00	IC .	LH0080A	CPU		
1		XA 03 70 03	и .	V9938	VOP(C)	·	
×		XA 83 40 01	н	S1985 (YM-3814)	MPS2		:
Ì		XA 91 30 01	и	SED9420C	D.Separator		
1		IG .05 73 00	8	MB8877M	FDC		
1		IG 09 89 00	B	HD74LS11	AND		†
1		IG 05 01 00	н	HD74LS175P	DFF		
1		IG 05 05 00	"	HD74LS368AP	ORIV	IC305	-
				HD74LS670P	R. FILE		
		IG 11 53 00		IR9311	Comparater		
	_	IG 13 49 00	<i>n</i>	NJM4558DV	OP Amp.		
		IG 00 13 90	. "	PST518A	RESET		
		IG 12 43 00	<i>n</i>		NAND		
		IG 02 69 10	n	HD74LS00P	INV	IC305	
		IG 02 70 10	"	HD74LS04P		10305	
	IC313	IG 05 91 00	ıı .	SN74LS06	INV/BUF		
	IC316	IG 04 37 50	0	SN74LS08N	AND	10000 010	
	IC315	IG 04 96 S0	, 1 =	SN74LS14N	INV .	IC308, 316	ļ <u>`</u>
	IC3S8	IG 04 97 50	n	SN74LS30N	NAND	IC341	
	IC304	IG 04 98 50	n	SN74LS32N	OR	C317, 346, 347	
	IC306	IG 04 42 00	ħ	HD74LS138P	3-8 DEC		
	IC314	IG 04 99 00	n	HD74LS139P	DEC		
	IC326	IG 06 00 50	<i>II</i>	SN74LS244N	DRIV		
	1C334	IG 04 46 00	Н	SN74LS245	TRAN.		
	IC325	IG 07 16 00	В	SN74LS367N	DRIV	IC330, 333	
	IC356	IG 06 03 50	<i>n</i>	SN74LS373N	LA TCH		
	IC314	XA 05 50 01	н	SN74ALS32N	OR	IC352	
	IC339	IG 05 10 00	и	TC40H004P	IN∀		
	IC345	IR 00 00 00	н	TC74HC00P	DIAND		
ж	IC319	IR 01 57 00	n	TC74HC1S7P	DATA-SE	IC322	
1	IC343	IG 14 22 00	И	TC74HCU04	INV		
	IC323	XA 45 70 01	И	MB81464-12	256K DRAM	C327,329,331,335,338,340,342	
*	IC320	XB 75 00 03	и	MSX2-BASIC INT	ROM		
*	IC328	XB 75 10 05	я	MSX2EX, OISC INT	н		
æ	1C332	XB 2S 60 02	u .	YRG G.8IOS-L	н		
æ	IC336	XB 25 70 02	ø	YRG G.BIOS-H	H		
*		XB 20 30 07	n	ARABIC-OS,V2.0	"		
	TR305		Transistor (TR307,308,310)	2SA933\$ R	トランジスタ		
	TR302		# (TR303,304,306,309)	2SC1740S R, S	JI .		
	D304	VA 93 09 00	Diode (D305)	1SS114	ダイオード		
	D301	iF 00 34 50	" (D302,303,306 ~ 313)				
	200.	FA 15 31 20	# (D302,303,300 ≈313) Mylar Cap.	0.0012 _H F 50V K	マイラーコン		
		FA 15 31 50	п	0.0015μF 50V K	п		t
		FA 15 33 30	· · · "	0.0033gF 50V K	n		
		FA 15 33 30 FA 15 41 10	"	0.01 _M F 50V K	"		
		FA 15 42 20	n n	0.022,/F 50V K	п		
			n n	0.047 ₄ F 50V K			
		FA 15 44 70		0.1µF 50V K	и и		
		FA 15 51 00	n Commin Con	15PE 50V K	セラコン		
		FG 41 11 50 FG 41 12 20		22PE 50V K	"		-
			H				

ランク: Japan only

Ref No.	Part No	""	Description	品 名	Remarks ランク
	FG 41 13 90	Ceramic Cap.	39PF 50V K	セ ラ コ ン	
	FG 41 16 80	n .	68PF 50V K	И	
	FG 44 41 00	n	0.01 _{/4} F 50V Z	"	
	UJ 13 82 20	Electrolytic Cap.	220µF 16V	ケミコン	
	UJ 12 82 20	n .	220 _{/4} F 10V	η	
	UJ 13 81 00	n	100 _{/4} F 16V	n	
	UJ 14 72 20	n	22, F 25V	п	
	UJ 12 81 00	n	100µF 10V	"	
	UJ 12 84 70	п	470μF 10V	η	
	UJ 16 81 00	н	1μF 50V	n	
	UJ 16 62 20	n	2.2µF 50V	n	
	UJ 16 64 70	"	4.7 _μ F 50 V	IJ	
	UJ 13 71 00	"	10μF 16V	n	
	UK 34 64 70	н	4.7 ₁₁ F 25V	вруказу	
	FZ 00 41 10	Semiconductive Cera.	0.1 a F 16V M	半導体セラコン	
TC301	VB 65 04 00	Variable Resistor	30PF CTZ-51F	可変コンデンサ	
	HZ 00 28 70	Resistor Array	RMLS4-103J	抵抗アレイ	
	HZ 00 46 60	# (RM308,310,316) 319,320	RMLS8-103J	п	
	HZ 00 51 20	" (RM315,317)	RMLS4-102J	п	5
	VA 06 97 00	n (RM313)	RMLS4-472J	n	
	VA 09 22 00	" (RM303.307.309) 311,312	RMLS8-223J	п	
	VB 65 00 00	JI	RMLS8-222J	п	
£301 ·	GE 30 03 50	Coil	68./ H	コイル	
	QU 00 92 00	Quartz Crystal Unit	21.4773MHz .	水晶振動子	
	VA 07 09 00	н	32.768KHz	n	
	VC 01 26 00	n	3 5795MNz	ħ	
	VB 65 06 00		16MHz	セラミック振動体	
	F: 36 32 20		LS MT X222MB	EMIフィルター	
	VB 45 27 00	В	EXC-EMT271T	В .	
	VB 45 28 00	В	EXC-EMT471T	В	
	VB 45 30 00	ıı .	EXC-EMT222J	н	
SW 1	VA 06 69 00	Push Switch	SPJ-312U	プッシュスイッチ	
RL301			DC AG4019	y v –	
	AA 55 40 00			VDPヒートシンク	
<u> </u>	LB 60 73 30			1 6 7 5 9 1	
	VB 64 55 00			PCジョイナー	
CN315	VB 09 77 00		8P	DINコネクタ	
	VB 24 54 00	"	13P TCS1001-01-202	И	
	VB 00 79 00	n	14P	アンフェノール	
	VB 32 12 00	"	50P D05-50\$A -1L1	J + 2 9	
	LB 60 80 50	- # (CN303)	9P	"	
	LB 91 80 30	` ' '	3P I-TYPE	ベースポスト	
	LB 93 20 40	n	4P	н	
_ ~	VB 32 11 00		50P	~ y 9 -	
5.1000	VB 31 70 00		13P	東線キット(Video)	Video Unit
	VB 31 70 00	n n	4P	n (FDD)	FDD PU
	VB 31 72 00	n .	34P	" (FDD)	FDD Unit
	VB 31 76 00	<u> </u>	50P	// (SLOT)	SLOT C.B
				- '	
	VB 84 79 00	Circuit Board	ROM Board	R O M > - 1	
		The second of th			
-					

★ : New Parts (NR)

ランク: Japan only

■ELECTRONIC PARTS

E E L	ECTRON	IC PARIS				
Ref. No.	Part No.		Description	品名	Remarks	ランク
	VB 22 59 00	Circuit Board	SLOT	S L O T > F	AX500	
	UJ 13 81 00	Electrolytic Cap.	100μF 16V	ケミコン		
	UJ 12 82 20		220 _R F 10V	n		-
	FZ 00 41 10	Semiconductive Cera. Cap	0.1 ₄ F 16V Z	半導体セラコン		
RM901		Resistor Array(PM902~904)	RMLS8-472J	抵抗アレイ		
LD1	VA 85 63 00	LED	TLR-208(red)	L E D		
	VB 73 84 00	PC Joiner		PCジョイナー		
CN905	LB 91 90 30	Base Post	3P L-TYPE	<u>ベースポスト</u>		
CN904	LB 91 80 40	, , , , , , , , , , , , , , , , , , ,	4P I-TYPE	н .		
_		Connector (CN9O2)	50P 479-25-30-142-1	<u> </u>		
CN903	VB 31 76 00	Wire Kit	50P 23768	東線 キット		
			2011		14 500	
	VB 84 79 00	Circuit Board	ROM Board	R O M > - 1	AA-300	
	XB 44 10 02	! IC	ROM	SAKHR MAIN (L)		
	XB 74 80 0	n	н	SAKHR SUB (H)		
	XB 74 90 0	-	н	ARABIC W/P		į
	FJ 72 81 00	Electrolytic Cap.		ケミコン		
		Semiconductive Cera. Cap.		半導体セラコン		
	HF 85 61 00	Resistor		抵 扰		
	VB 90 62 00	Header		ヘッダー		
					141 44	
					· · · · · · · · · · · · · · · · · · ·	
	<u> </u>					-
	1					
	_			-		
	<u> </u>					
					. '	
						<u> </u>
					-	—
L.—					+	_
		<u> </u>			0)
L	_				+ ·	_ + _
				As-		
<u> </u>		<u></u>	· ·			
	1/			-		
<u> </u>		<u> </u>				
1						
	-				-	
		, ,				
4	3					

ランク Japan only

MAX-500 POWER SUPPLY UNIT ELECTRONIC COMPONENTS (VB657500)

Ref No.	Part No	1	Description		品	名	Remarks	ラング
IC101	ix 55 31 10	<u> </u>	IR9431		シャフトレギ	ュレータ		- `
	1 iX 55 44 00		PC817		フォトス	プラ		
PC10	1 1x 55 44 0t	 "						1
	'Y 55 44 16	Translator	2SC3376	-	トラン	シスタ		
Q101			2SC2655-Y	,	"			
0102			2SC1815-0		п			
Q104,1		<u> </u>	2SA1015-0					
Q106			2SC3303-Y			$\overline{}$	-	
Q104		·	2SC2877-Y		- "			
0107	ix 55 31 50	<u> </u>	2302077-1					
		<u> </u>			ブリッジダ	4 + - K		
D101			\$1WB60		ファーストリカバ		·	
D102		+	F1-08				,	
D103	iX 55 31 8	<u> </u>	155144		シリコンダ	1 7 - r		
D104,1	05 iF 00 <u>06 7</u> 0	0	182473		"			-
D106	iX 55 31 7	0 "	ESAC82M-		ショットキーバリ			
D107	7 iX 55 44 30	o "	SM-3-02FF	RLF	ファーストリカ/	リタイオード	<u> </u>	
D108,1	109 IX 55 39 2	0. "	F1-02		"			_—
	_1				ļ			_ —
T101	GX 55 06 2	O Switching Transformer			スイッチング		El-35	
L101			752YOR4		スモンモー	チョーク		
L102	GX 55 04 3	O Coil (Rod type)	10 _µ H	棒状	<u>チョーク</u>	コイル		
_	04 GX 55 04 4		10µH	ドラム				
-					1			
0102	103 FX 55 15 8	0 Ceramic Cap.	0.0022µF	4KV	セラミック:	コンデンサ	TYPE KD	
C106			0.00033µI	1KV	n n		TYPE KD	
10,00	- 1X 33 13 3	-		-	1			
C10	1 FX 55 10 8	<u> </u>	0.1 _µ F	250VAC	メタライズド	コンデンサ	TYPE VE	
C10!			0.01 _µ F	630V	ıı ıı		TYPE MMH	
C 10:	5 7 55 10 0	-			+			
1:	7 51 40 40 3		0.033μF	50V	ポリエステル	コンデンサ	TYPE AMZ	
C10			0.047 _µ F		- "		<i>n</i>	
_	110 FX 55 12 1		0.1 <i>µ</i> F		Н Н		n	
C10	9 FA 15 51 C		U.1/E		 			
			— 150 - -	400V	電解コン			
C10		00 Electrolytic Cap.	150µF		40 m - 1			
C11			4700µF	10V	- "			
C11			2200aF	<u>"</u>	·			-
C113~	116 UJ 42 83 3		330 _µ F	<u>"</u>				
C11	9 UJ 13 81 Q	n	100 _µ F	16V				
C11	8 <u>uw 67 64 7</u>	<u>"</u>	.4 7μF	63V	<u> </u>	_		
C11	7 UJ 16 61 0	90	1 µF	_50V			 	
					<u> </u>			
R10	8 HL 32 61	Metal Oxide Resistor	1.5Ω	2W	酸化金属			
R106	,107 HL 32 61 (00 "	1ΚΩ					
	,103 HL 32 76 8		68KΩ	<i>"</i>				
1-	.105 HL 32 81 (100ΚΩ	"	- 1		-	<u>_</u>
	,111 HX 55 17 4		47Ω	3W	1			
1								
B11	6 HL 31 52 1	20 Carbon Resistor	220Ω	1/2W	カ ー ボ	ン抵抗		
_	,118 HL 31 58		820Ω	н		,		
			10ΚΩ	"	7	,		
R12			10Ω	1/4W	,	ı —	T:	
R11	3 FJ 30 41 5	70 "	47Ω	- 17 - 17	- +	,	1	

₩ : New Parts (NR)

ランク Japan only

■ AX-500 POWER SUPPLY UNIT ELECTRONIC COMPONENTS

Ref No.	Part No.		Description	品 名	Remarks	ラン:
114	HJ 35 45 6	Carbon Resistor	56Ω 1/4W	カーポン抵抗		
	НЈ 35 51 0		100Ω "	"		
	HJ 35 52 7		270Ω "	"		
	HJ 35 53 3		3300 "	"		
	нЈ 35 55 6		560Ω "	"		
			1.2ΚΩ "	и		
	HJ 35 63 0		3KΩ "	и		
	HJ 35 63 6		3.6ΚΩ "	. "		
	HJ 35 81 0		100ΚΩ "			
101	HX 55 16 4	0 Cement Resistor	3.3Ω 3W	セメント抵抗		
R101	HX 55 14 0	0 Variable Resistor	1ΚΩ	半 固 定 抵 抗	. , -	
	LX 55 06 9	O Fuse Clip		ヒューズクリップ	1	
	KB 00 23 6	0 Fuse	T2A 250V	k = - X		
-	LX 55 06 0	0 Connector	5277-02A	コ ネ ク タ ー 2P		
	NX 55 06 3			コネクター3P Ass'y		
-	NX 55 06 4			コネクター4P Ass'y		
	AX 55 02 8	0 Support Transistor		サポートトランジスタ		
	CX 55 60 2	O Heat Sink Tube	サーコンFR, t=0.45	チューブタイン		
				放熱板		
	BX 55 00 9 BX 55 01 0		Al, 2t	H H		
				# 45 # A 41 # 4 # A	50/F #L##F	
		6 Wesher Pan Head Screw	M3×8プラス	歯付座金付ナベルネジ		
		6 Bind Heed Screw	M3×8プラス	バインド小ネジ		
	Ei 03 01 4	6 Bind Head Tapping Screw	3×14パインドプラス	タッピングネジ	サポートトランジスタ	
						
					· · · · · · · · · · · · · · · · · · ·	
	<u> </u>	<u> </u>				
	T					i_
]		
			,,,			
			*			
	 			1.		
	 					ļ.
			-· +	*		-
	-					
				· · · · · · · · · · · · · · · · · · ·		
		· · · · · · · · · · · · · · · · · · ·	-			

₩ : New Parts (NR)

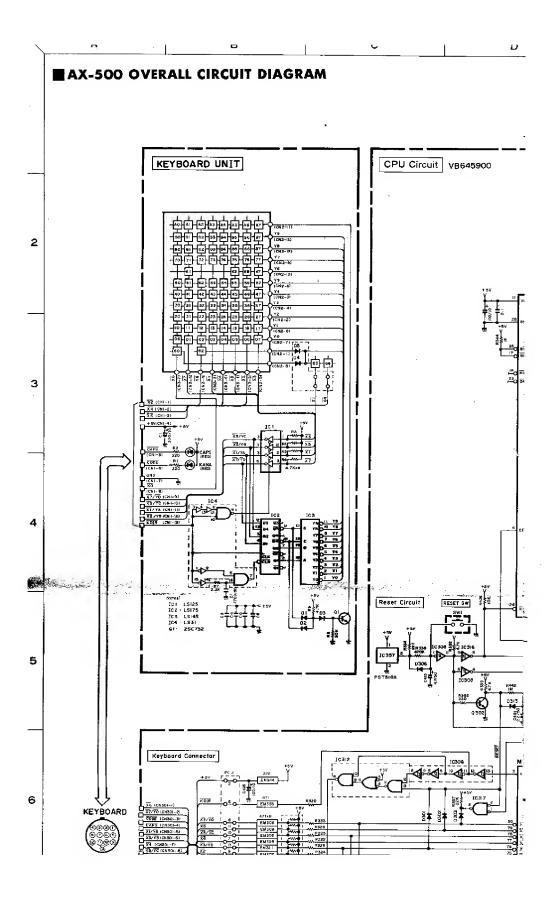
ランク: Japan only

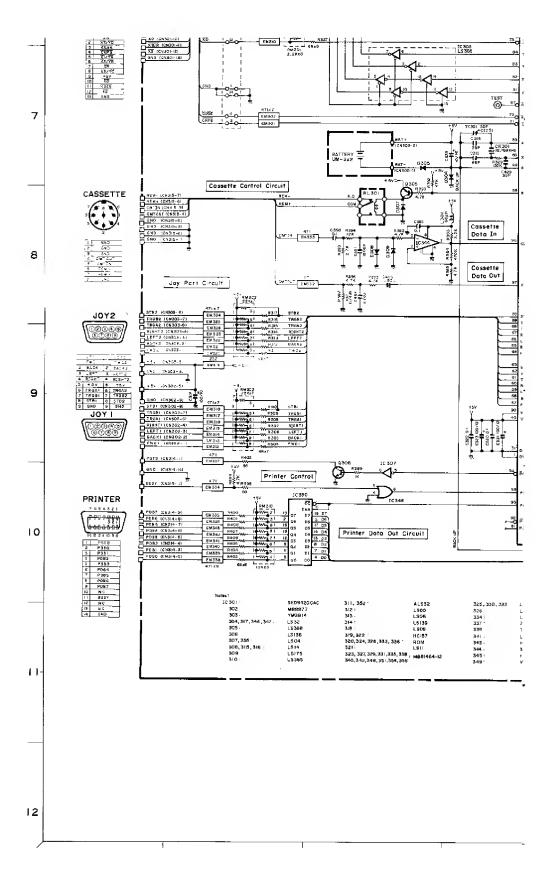
■ AX-500 VIDEO MODULE UNIT ELECTRONIC COMPONENTS (VB657700)

ef. o.	Part No.		Description	品名	Remarks ラン
	iG 02 70 10	IC	74LS04	I C	IC201
_	iX 55 28 90	п	LVA510	"	IC202
	HJ 35 41 80	Resistor	18Ω 1/4W	抵抗	
	HF 85 46 80	<u>"</u>	68Ω 1/6W		R217,222
	HF 85 52 20	n	220Ω "	#	R201,202,205,216,256
	HF 85 55 60	<i>"</i>	560Ω "	"	R246,254
	HF 85 56 80	n	680Ω "	, , , , , , , , , , , , , , , , , , ,	R204,240
	HF 85 58 20		820Ω "	<i>u</i>	R210
	HF 85 61 00	<u> </u>	1ΚΩ "	<i>y</i>	R207,209,223,228,229,231,234,236
	HF 85 61 80	n	1.8ΚΩ "	n n	R203,224,255
	HF 85 62 20	<i>n</i> -	2.2ΚΩ π	<i>B</i>	R249,253
_	HF 85 63 30	и .	3.3ΚΩ #	D	R206
	HF 85 66 80		6.8ΚΩ #	n	R225
_	HF 85 71 00	н	10ΚΩ	п	R214,218,233,248
	HF 85 71 50	#	15ΚΩ π	n	R215
	HF 85 72 20		22ΚΩ "	H	R212
	HF 85 73 90	н	. 39KΩ "	и	R230,252
	HF 85 75 60	"	56KΩ "	n n	R231,251
	HF 85 76 80	н	68ΚΩ #	"	R250
			1.8KΩ or 4.7KΩ		R245
	UJ 12 81 00	Electrolytic Cap.	100µ 10V	ケミコン	C207,210,233
	UJ 12 83 30	"	330µ #	<i>y</i>	C211,227~229
	UJ 13 71 00	n	10u 16V	n	C201~204,217
	UJ 13 81 00	η	100µ "	"	C205,231
	UJ 16 61 00	n	1 _{/t} 50V	"	C216,218
	FG 21 11 80	Ceramic Cap	18P	セラコン	C237
	FG 21 12 20	н	22P	n	C238
	FG 21 15 60	n	56P	и	C213
	FG 21 13 90	n	39P	n	C212,220,236
	FG 21 21 00		100P	"	C215,221
	FG 21 21 50	n	150P	н	C219,225
	FG 21 22 20	п	220P	, , , , , , , , , , , , , , , , , , ,	C222,223
	FG 21 24 70		470P	n	C209.234
	FG 41 31 00		1000P	п	C215,235
	FG 44 42 20	n	22000P	'n	C206,208,209,214,230,232
	IC 23 20 30	Transistor	2SC458	 トランジスタ	0204,205,207,208,110,111
	15 23 20 30	#	2SC1684		Q201~203,206
			2SC1317	"	Q209
-	iX 55 30 90		1SS106	ダ イ オ ー F	D201~203
	IX 80 11 90	II	1SS119	- / / /	D204~207
	N 60 11 90	.			
		Present Potentiometer	2K	プリセット	VR201
	HX 55 13 80	_ / /	10K	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	VR202
	GX 55 03 60	Inductor	22 _n H	インダクター	L201
	GX 55 06 00		33 _n H	и	L202
	QX 55 00 40	Quartz Resonator	4.433619MHz	水晶振動子	X201
	KX 55 04 30	Slide Switch	- c-	スライド SW.	SW201
_		-			
					-

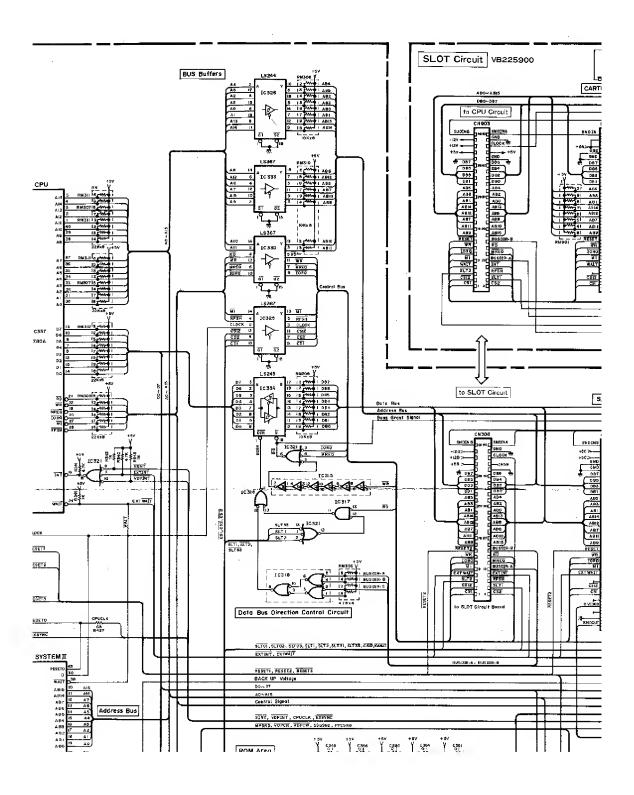
※ New Parts (NR)

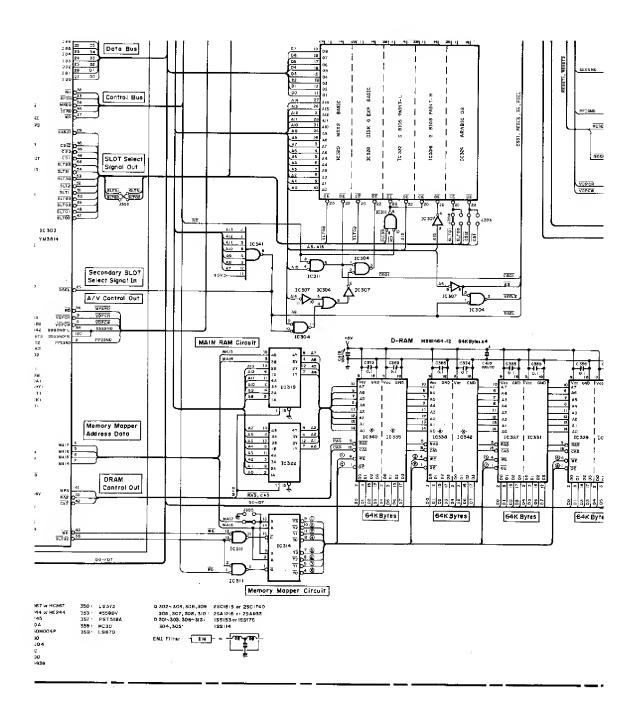
ランク Japan only



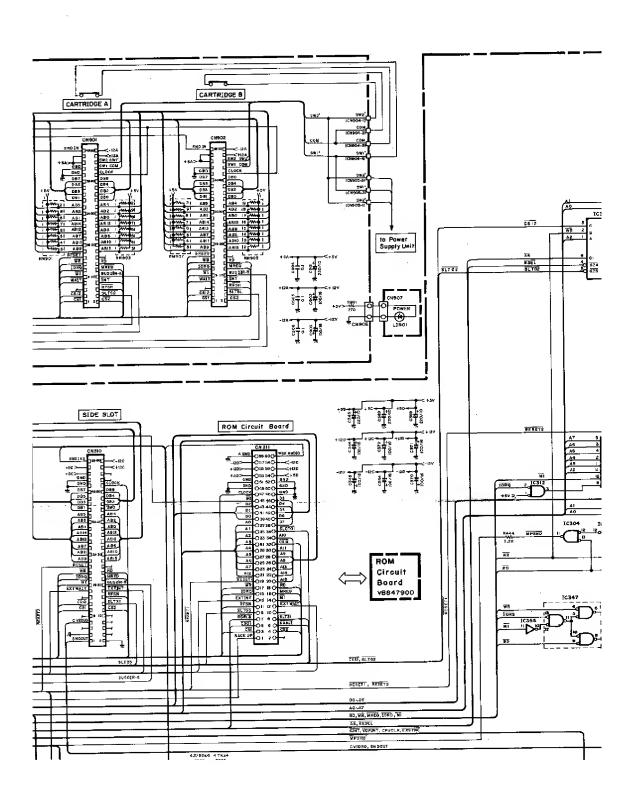


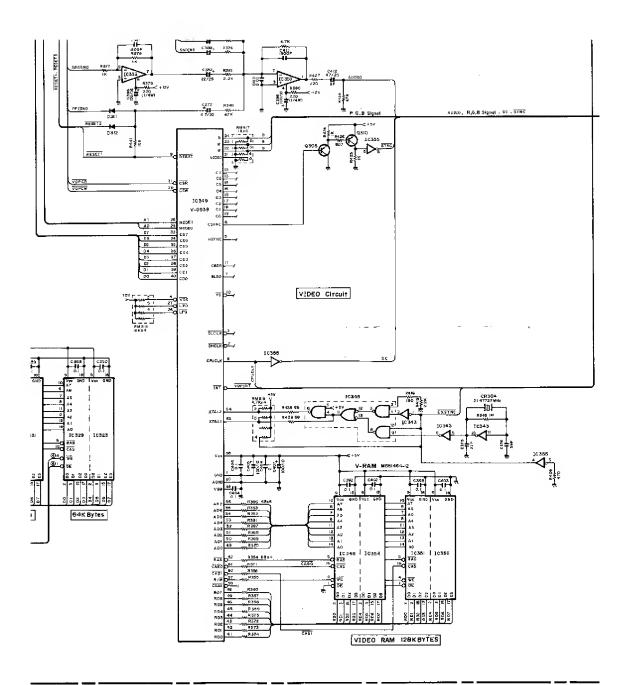
Р 9 П



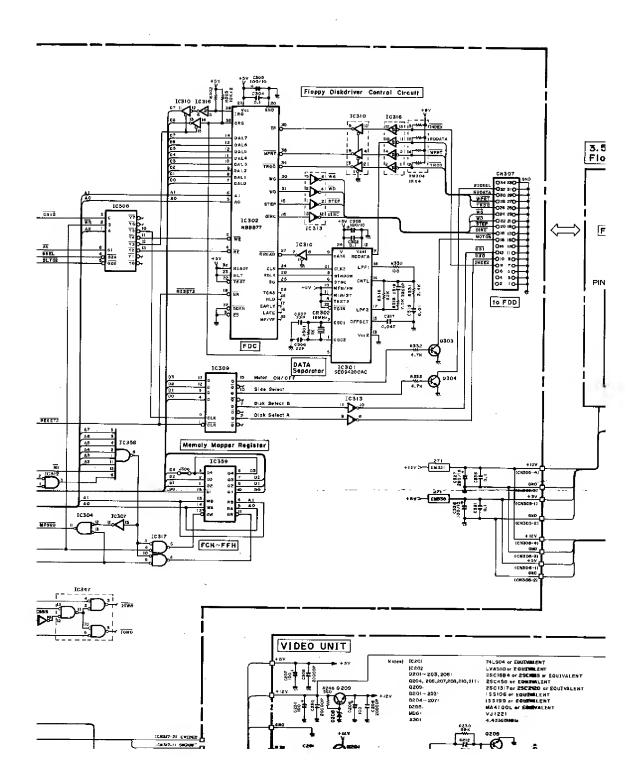


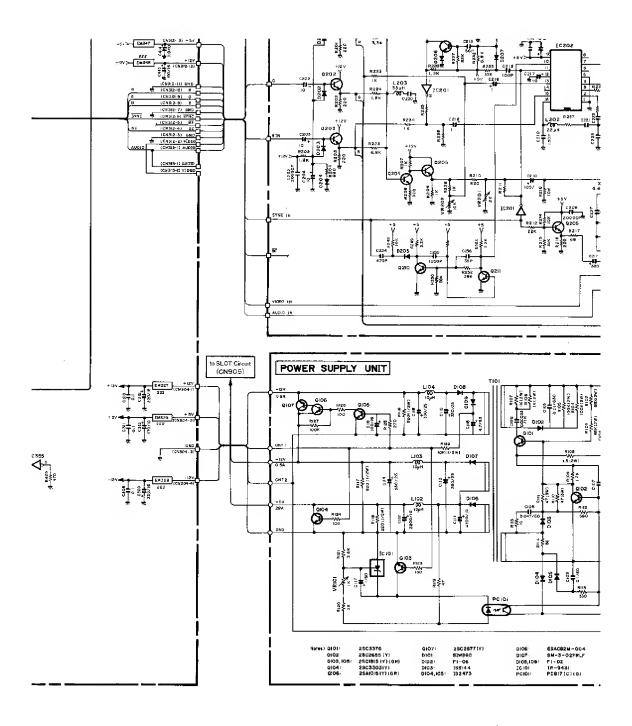
н І І К І

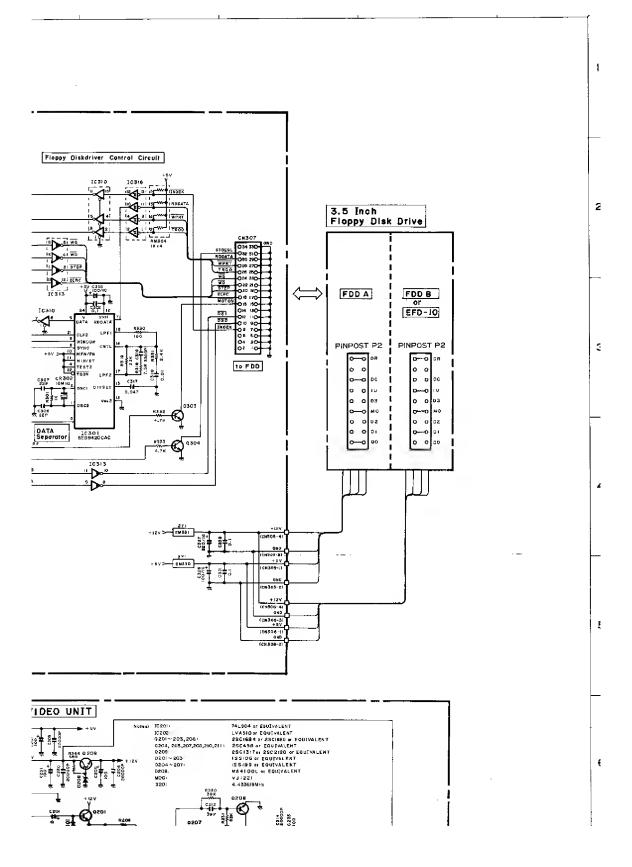


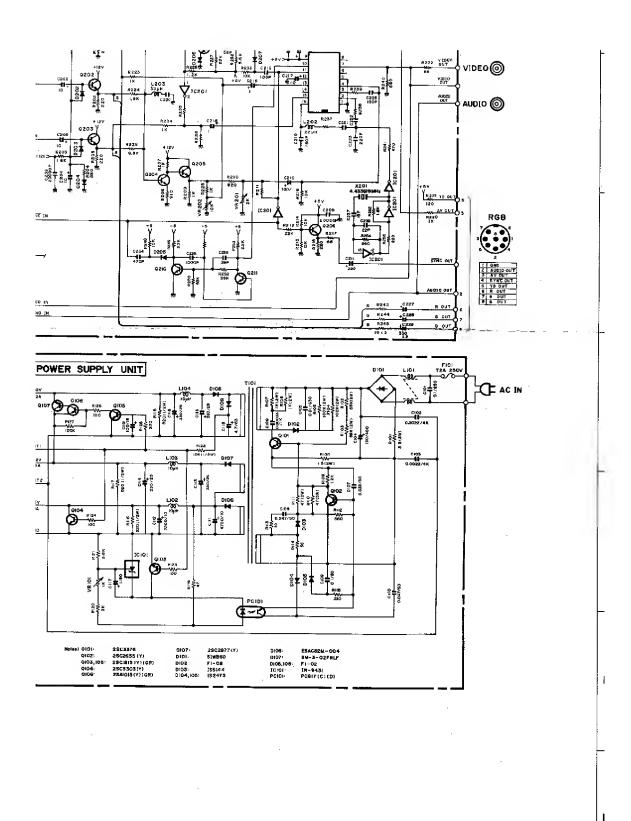


L





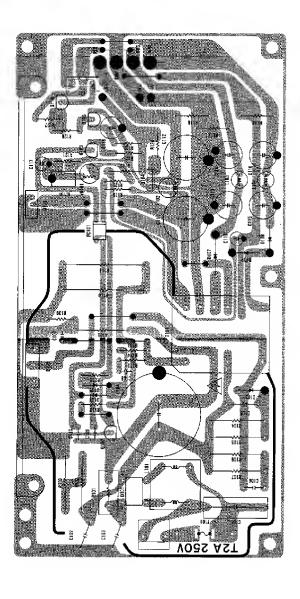




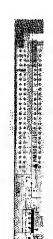
0

Q

Ν



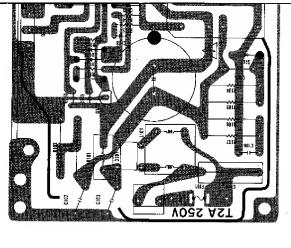
SUB ROM CIRCUIT BOARD

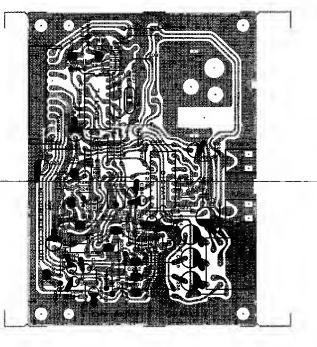










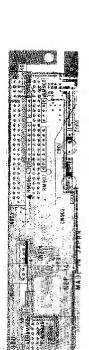


SLOT CIRCUIT BOARD

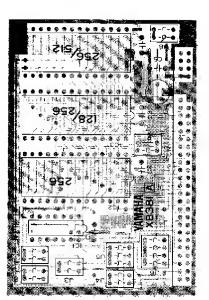


■ SUB ROM CIRCUIT BOARD

A STATE OF THE S



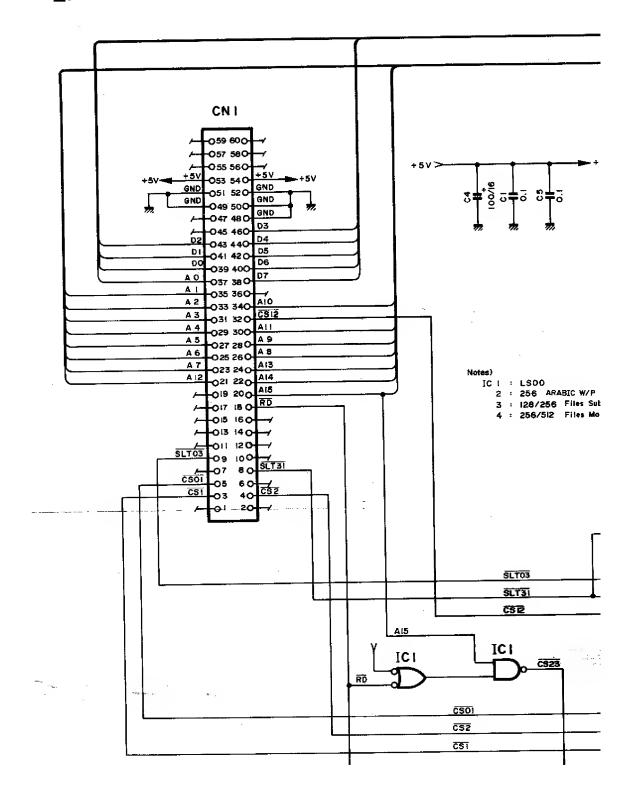
۵

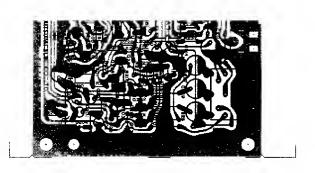


Note 4

Connect each +5V side of C311 and C320 with a jumper wire.

PC joiner (50mm 14 core) VB64550





SLOT CIRCUIT BO



